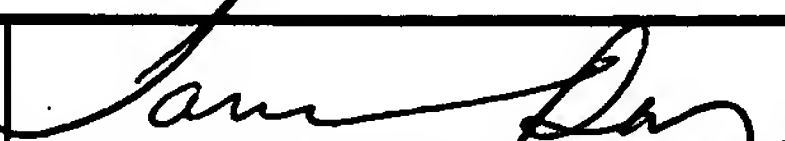


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		Application Number	10709234		
		Filing Date	2004-04-22		
		First Named Inventor	Rakshit SINGHAL		
		Art Unit	2182		
		Examiner Name	Tammara R. PEYTON		
Sheet		of		Attorney Docket Number	TI-36210

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
AP	1	SINGHAL, Rakshit et al.; A Novel Mechanism to Optimize Memory to Memory Data Transfers in a System-on-Chip Using Store and Forward Bridge and a DMA Engine; pages 1-6	
JP	2	Fact Sheet: AR7Wi Wireless ADSL Router; pages 1-4; Texas Instruments Real World Signal Processing; 2004	
JP	3	PAREKHJI, Nikita et al.; DFT For Test Optimisations in a Complex Mixed-Signal... ; 2004 IEEE ITC International Test Conference; pages 773-782	

Examiner Signature		Date Considered	12/20/06
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